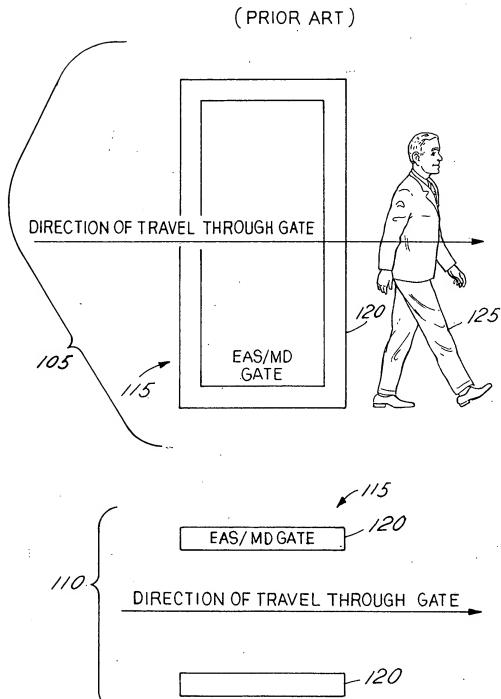
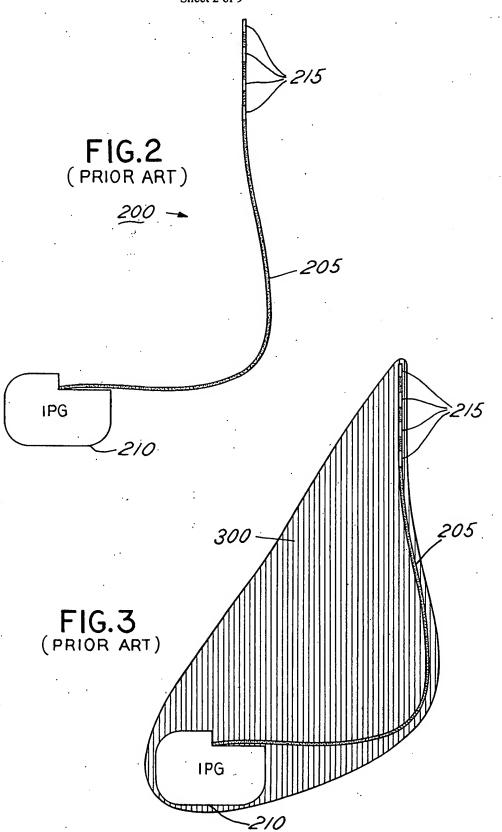
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FIG.I



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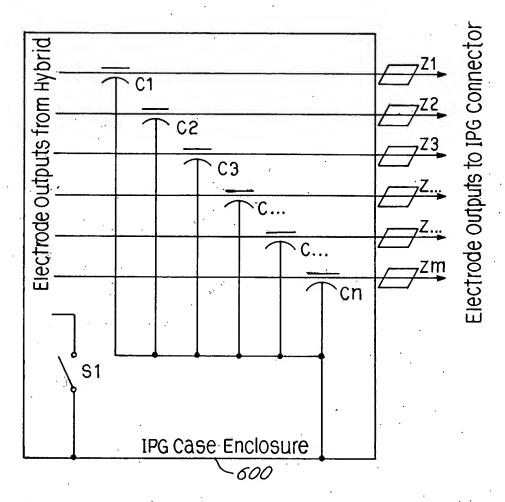


Binal J. Patel - 312.715.1000 Attorney Docket 11738.00041 Serial No. 10/034,945 Inventor: Hrdlicka et al. Sheet 3 of 9 FIG.4 (PRIOR ART) IPG CASE 520B 507 510 FIG.5 (PRIOR ART) IPG CASE 505

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FIG.6



C1 thru Cn = Feedthrough Capacitors (part of feedthrough or separate capacitors)

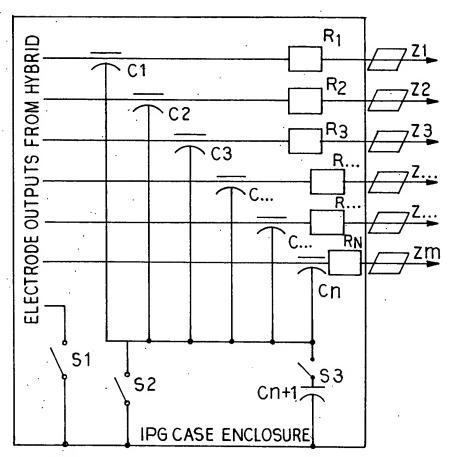
Cn+1=common EMC capacitor to IPG case

Z1 thru Zm = Impedance elements on outboard side of feedthroughs (may be ferrite bead, resistor, or inductor)

S1=case electrode switch(may be electronic or mechanical such as a reed switch)

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C1 thru Cn=
Feedthrough Capacitors
(part of feedthrough or separate capacitors)
Cn+1=single case electrode

Z11hru Zm=AC current blocking element Impedance elements on outboard side of feed throughs capacitors (may be ferrite bead, resistor, or inductor)

s1 = optional switching device

s2 = optional switching device

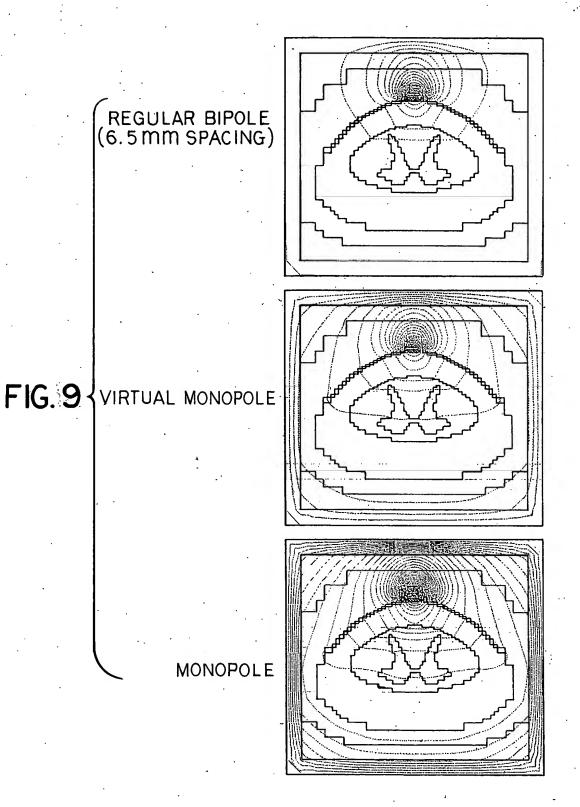
s3 = optional switching device

R1-RN = optional resistors

ELECTRODE OUTPUTS TO IPG CONNECTOR

Attorney Docket 11738.00041 Serial No. 10/034,945 Inventor: Hrdlicka et al. Sheet 6 of 9 800 CURRENT LIMITING ELEC. 850 STIMULATION ELECT. POWER SOURCE 820 MEMORY 840 830 COMM. ELEC. 835 ~IPG 805 SENSOR 825 IPG CASE — 845 SIGNAL GENERATOR 855

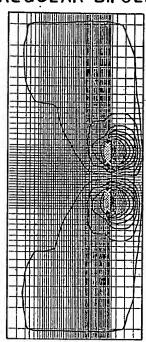
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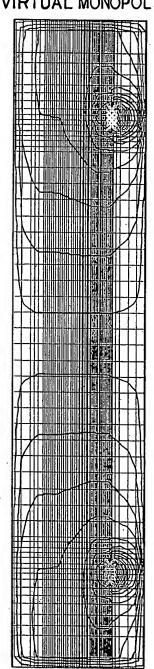
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FIG.IO

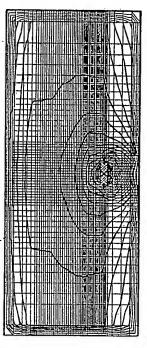
REGULAR BIPOLE



VIRTUAL MONOPOLE



MONOPOLE.



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